NOTE: This order is nonprecedential.

United States Court of Appeals for the Federal Circuit

SEMICONDUCTOR DESIGN TECHNOLOGIES, LLC,

 $Plaintiff ext{-}Appellant$

 $\mathbf{v}.$

CADENCE DESIGN SYSTEMS, INC.,

Defendant-Appellee

2024-2026

Appeal from the United States District Court for the Northern District of California in No. 3:23-cv-01001-RFL, Judge Rita F. Lin.

ORDER

The parties having so agreed, it is ordered that:

(1) The proceeding is DISMISSED under Fed. R. App. P. 42 (b).

- 2 SEMICONDUCTOR DESIGN TECHNOLOGIES, LLC V. CADENCE DESIGN SYSTEMS, INC.
 - (2) Each side shall bear their own costs.

FOR THE COURT

Jarrett B. Perlow Clerk of Court

<u>July 19, 2024</u> Date

ISSUED AS A MANDATE: July 19, 2024